

BTW69-1200

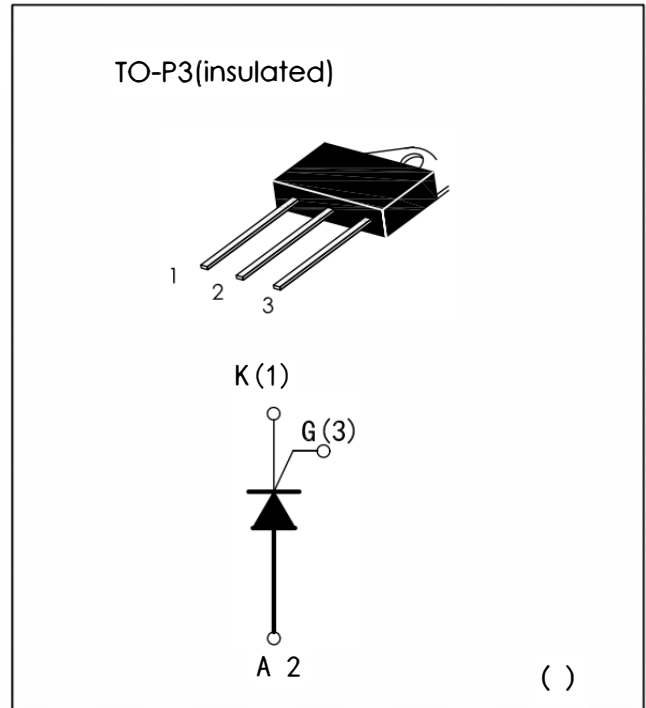
SCRs

DESCRIPTION:

High current density due to double mesa technology.
series of silicon controlled
rectifiers are specifically designed for high power
switching and phase control applications.
series are suitable for general
purpose applications, a high gate sensitivity is
required.
provides a 2500V RMS
isolation voltage from all three terminals to external
heatsink.

MAIN FEATURES

Symbol	Value	Unit
$I_T(RMS)$	50	A
IGT	≤ 80	mA
V_{TM}	≤ 1.8	V



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		Tstg	-40 to +150	°C
Operating junction temperature range		Tj	-40 to +125	°C
Repetitive Peak Off-state Voltage Tj=25°C Repetitive Peak Reverse Voltage Tj=25°C		VDRM	1200	V
	BTW69-1200	VRRM		
RMS on-state current (all conduction angles)	TO-P3 Tc=80°C	IT(RMS)	50	A
Average on-state current (half sine wave)	TO-P3 Tc=80°C	IT(AV)	40	A
Non repetitive surge peak on-state current (half sine cycle, Tj=25°C)	f = 50 Hz t=10ms	ITSM	520	A
	f = 60 Hz t=8.3ms		540	
I²t Value for fusing	tp=10ms	I²t	1350	A²s
Repetitive rate of rise of on-state current after triggering ITM=20A IG=50mA dIG/dt 50mA/ms		dIT/dt	150	A/μs
Peak gate current	tp=20us, Tj=125°C	IGM	1.5	A
Peak gate power	tp=20us, Tj=125°C	PGM	10	W
Average gate power dissipation	Tj=125°C	PG(AV)	2	W

ELECTRICAL CHARACTERISTICS(Tj=25℃ unless otherwise specified)

Symbol	Test Condition	BTW69-1200		Unit
			MAX.	
IGT	VD=12V RL=33Ω		60	mA
VGT		1.5		V
VGD	VD=VDRM RL=3.3KΩ Tj =125℃	0.2		V
IL	IG=1.2IGT		100	mA
IH	IT =500mA		80	mA
dV/dt	VD=67%VDRM gate open Tj=125℃		1000	V/μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
VTM	ITM=80A, tp=380μs	Tj=25℃	1.8	V
IDRM IRRM	VD=VDRM VR=VRRM	Tj=25℃	10	μA
		Tj=125℃	8	mA

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
Rth(j-mb)	thermal resistance from junction to mounting base			℃/W
		TO-P3	0.65	
Rth(j-hs)	thermal resistance from junction to heatsink with heatsink compound			
		TO-P3	0.9	